

OPERATING TEMPERATURE

Operating	NPO (COG)	X7R	Y5V (Z5U)
Temperature	-55℃~+125℃	-55℃~+125℃	-30℃~+85℃

PN Structure

THE A

<u>WCD</u>	<u>160</u>	<u>M</u>	<u>103</u>	<u>1206</u>	N
<u>Series</u>	<u>Voltage</u>	<u>Tolerance</u>	<u>Capacitance</u>	<u>Size Code</u>	<u>Dielectric</u>
	<u>1</u>	<u>2</u>	<u>3</u>	<u>4</u>	<u>5</u>

1. Voltage

	-
160	16V
250	25V
500	50V
630	63V

2. Tolerance

А	В	С	D	F	G	Н	J	K	М	Z
± 0.05pF	±0.1pF	±0.25pF	±0.5pF	±1%	±2%	±2.5%	±5%	±10%	±20%	+80%- 20%

3. Capacitance

103	10x10 ³ pF
4R7	4.7pF

4. Size Code: Chip Size (LxW)

0402	0.04x0.02 Inches
0603	0.06x0.03 Inches
0805	0.08x0.05 Inches
1206	0.12x0.06 Inches

5. Dielectric

N	NPO
В	X7R
Х	X5R
Y	Y5V



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Capacitance and Capacitance Tolerance

Different circuit needs different capacitance and capacitance tolerance. So the selection of capacitance is depended on the need of customers.

Dielectric Material Type of Capacitor

- NPO: The capacitor of this kind dielectric material is considered as Class I capacitor, including general capacitor and high frequency NPO capacitor. The electrical properties of NPO capacitor are the most stable one and have little change with temperature, voltage and time. They are suited for applications where low-losses and high-stability are required, such as filters, oscillators, and timing circuits.
- X7R, X5R: X7R, X5R material is a kind of material has high dielectric constant. The capacitor made of this kind material is considered as Class II capacitor whose capacitance is higher than that of class I. These capacitors are classified as having a semi-stable temperature characteristic and used over a wide temperature range, such in these kinds of circuits, DC-blocking, decoupling, bypassing, frequency discriminating etc.
- Y5V: The capacitor made of this kind of material is the highest dielectric constant of all ceramic capacitors. They are used over a moderate temperature range in application where high capacitance is required because of its unstable temperature coefficient, but where moderate losses and capacitance changes can be tolerated. Its capacitance and dissipation factors are sensible to measuring conditions, such as temperature and voltage, etc.

Voltage

16 VDC, 25 VDC, 50 VDC, 63 VDC

Capacitance

0.1pF ~ 100uF

Terminations

Tin / Nickel

Tolerance

 $\pm 0.05 pF$, $\pm 0.1 pF$, $\pm 0.25 pF$, $\pm 0.5 pF$, $\pm 1\%$, $\pm 2\%$, $\pm 2.5\%$, $\pm 5\%$, $\pm 10\%$, $\pm 20\%$, $\pm 80\%$ -20%

Packing

Tape and Reel (0402, 0603, 0805, 1206, 1210, 1812, 2220)

Dielectric & Values

NPO X7R Y5V Z5U consult product pages of catalog for cap ranges and voltage rating



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Specification and Test Condition:

1. Appearance

Dielectrics	Specification	Testing Condition	
NPO/X7R/X5R/Y5V	No defects or abnormalities	Visual inspection	
2. Dimensions			
Dielectrics	Specification	Testing Condition	
NPO/X7R/X5R/Y5V	Within the specified dimensions	Using calipers on micrometer	
3. Capacitance			
Dielectrics	Specification	Testing Condition	
NPO	Within the specified tolerance B:±0.1pF;C:±0.25pF;D:±0.5pF;J:±5%	1.0±0.2Vrms, 1MHz±10% (C>1000 pF, 1.0±0.2Vrms, 1KHz±10%,)25℃	
X7R/X5R	Within the specified tolerance J: ±5% K: ±10%M: ±20%	1.0 ± 0.2 Vrms, 1KHz $\pm10\%$ (Cp >10 uF,0.5 ±0.1 Vrms,120 ±24 Hz)at 25 $^{\circ}$ C,48hrs after annealing	
Y5V Within the specified tolerance M: ±20%; Z: +80 ~ -20%		1.0 \pm 0.2Vrms, 1KHz \pm 10% (Cp $>$ 10uF,0.5 \pm 0.1Vrms,120 \pm 24Hz)at 25 $^{\circ}$ C, 48hrs after annealing	
4. Dissipation Factor	•		
Dielectrics	Specification	Testing Condition	
	Cp<30pF, Q≥400+20Cp;	1.0±0.2Vrms,1MHz±10% ,25℃	
NPO	Cp≥30pF, Q≥1000	(Cp>1000pF,1.0±0.2Vrms,1KHz±10%)	
	VR≥25V, DF ≤2.5%	1.0±0.2Vrms, 1KHz±10%,	
X7R/X5R	VR =16V, DF ≤3.5%	(Cp>10uF,0.5±0.1Vrms,120±24Hz)	
	VR ≤10V, DF ≤5.0%	at 25 °C,48hrs after annealing	
	VR≥25V, DF ≤7.0% (C<1.0μF)	1.0±0.2Vrms, 1KHz±10%,	
Y5V	VR =16V, DF ≤9.0%	(Cp>10uF,0.5±0.1Vrms,120±24Hz)	
	VR ≤10V, DF ≤12.5%	at 25℃,48hrs after annealing	
5. Insulation Resista	nce	•	
Dielectrics	Specification	Testing Condition	
NPO/X7R/X5R/Y5V	More than 10 G Ω or 500 Ω ·F, whichever is smaller.	Rated voltage for 60±5sec, at 25 $^\circ\!\!\!{}^\circ\!\!\!{}^\circ\!\!\!{}^\circ$	

6. Dielectric Strength			
Dielectrics	Specification	Testing Condition	
NPO /X7R/X5R/Y5V	No defects or abnormalities.	No failure shall be observed when 300% (NPO);250% (X7R/ X5R/Y5V)of the rated voltage is applied between terminations for 1 to 5 seconds, provided the charge /discharge current is less than 500mA	



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7. Temperature Coefficient of Capacitance

Dielectrics	Specification	Testing Co	ndition			
		Measure capacitance under follow table list temperature:				
NPO	Temperature coefficient within ± 30 ppm/ $^{\circ}$ C p drift within	STEP	NPO, X7R	X5R	Y5V	
NF O	±0.2% or ±0.05pF	1	25 ±2	25 ±2	25 ±2	
		2	-55±3	-55±3	-30±3	
	R Capacitance change within ±15%	3	25 ±2	25 ±2	25 ±2	
X7R/X5R		4	125±3	85±3	85±3	
		5	25 ±2	25 ±2	25 ±2	
Y5V	Capacitance change within +22%, -82%	differences values in th The temper Capacitance 2) X7R ,X5R The ranges above 25 °C	tance drift is calcula between the maximu te step 1,3 and 5. rature coefficient is d e measured in step 3 and Y5V of capacitance change value over the tempo specified ranges.	m and minimum me etermined using the as a reference. ge compared within	e n the	

8. Adhesion

U. Aunes		
Dielectrics	Specification	Testing Condition
NPO		The pressurizing force shall be 10N (=1000g*f) and the duration of application shall be 10 ± 1 sec.
		\ hooked jig \
X7R/X5R	No removal of the terminations or other defect shall occur.	r=0.5
Y5V		Chip Chip

9. Solderability of Termination

Dielectrics	Specification	Testing Condition		
NPO				
X7R/X5R	95% min. coverage of both terminal electrodes and less than 5% have pin holes or rough spots.	Solder temperature: 230±5°C Dipping time: 2±1 seconds. Completely soak both terminal electrodes in solder		
Y5V	5% have pin holes of fough spots.	terminal electrodes in solder		

10. Resistance to leaching

Dielectrics	Specification	Testing Condition		
NPO		6 11 1 270 500		
X7R/X5R	95% min. coverage of both terminal electrodes and less than 5% have pin holes or rough spots.	Solder temperature: 270±5°C Dipping time: 10±1 seconds.		
Y5V	No remarkable visual damage.	Completely soak both terminal electrodes in solder		



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11. Bending

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change $\leq \pm 5\%$ or ≤ 0.5 pF	Solder the capacitor on testing substrate and put it on testing stand. The middle part of substrate shall successively be pressurized by pressuring rod at a rated of about 1.0mm/sec. Until the deflection
X7R/X5R	No remarkable visual damage Cp change $\leq \pm 12.5\%$	become means of the 1.0mm.
Y5V	No remarkable visual damage Cp change $\leq \pm 30\%$	capacitance meter 45 4 45

12. Resistance to Soldering Heat

Dielectrics	Specification	Testing Condition
	No remarkable visual damage	
	Cp change within $\pm 2.5\%$ or $\pm 0.25pF$,	
NPO	whichever is larger.	
NPO	DF meets initial standard value.	Soldering temperature: 270 \pm 5 $^{\circ}$ C
	IR meets initial standard value.	Preheating: $120 \sim 150^{\circ}$ 60sec. Dipping time: 10 ± 1 seconds.
	No remarkable visual damage	Measurement to be made after being kept at room temperature for
	Cp change within ±5%	24±2 (COG) or 48±4(X7R ,X5R, Y5V) hours. Recovery for the following period under the standard condition after
X7R/X5R	DF meets initial standard value.	test.
	IR meets initial standard value.	*Initial measurement for high dielectric constant type Perform a heat treatment at $140 \sim 150^{\circ}$ for 1hr and let sit for 48 ± 4 hrs at room
	No remarkable visual damage	temperature. Perform the initial measurement.
Y5V	Cp change within ±20%	
121	DF meets initial standard value.	
	IR meets initial standard value.	

13. Temperature Cycle

Dielectrics	Specification	Testing C	Testing Condition		
		To perform 5 cycles of the stated environment:			
	No remarkable visual damage	Step	Temperature	Time	
NPO	Cp change within $\pm 2.5\%$ or ± 0.25 pF,	1	Min. operating Temp.+0/-3℃	30min	
	whichever is larger.	2	25℃	2~3 min	
		3	Max. operating Temp.+0/-3°C	30 min	
		4	25℃	2~3 min	
X7R/X5R	No remarkable visual damage Cp change within ±7.5%	Measurement to be made after being kept at room temperature for 24±2hrs (COG) or 48±4hrs (X7R, X5R, Y5V) at room temperature, then measure. *Initial measurement for high dielectric constant type Perform a heat treatment at 140~150°C for 1hr and let sit for 48±4hrs at room temperature. Perform the initial measurement.			



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14. Moisture Resistance ,steady state

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change within $\pm 5\%$ or ± 0.5 pF, whichever is larger. Cp < 10 pF, Q $\geq 200+10$ Cp; IO \leq Cp < 30 pF, Q $\geq 275+2.5$ Cp Cp ≥ 30 pF, Q ≥ 350 R*C ≥ 1000 M Ω or 50 Ω •F, whichever is smaller	Test temperature: 40±2°C Humidity: 90~95% RH Testing time: 500 ±12hrs Measurement to be made after being kept at room
X7R/X5R	Cp change within $\pm 12.5\%$ DF: Not more than 2 times of initial value R*C≥1000M Ω or 50 Ω •F, whichever is smaller	temperature for 24±2hrs (COG) or 48±4hrs (X7R, X5R, Y5V) *Initial measurement for high dielectric constant type
Y5V	No remarkable visual damage Cp change within $\pm 30\%$ DF: Not more than 1.5 times of initial value R*C≥1000MΩ or 50Ω•F, whichever is smaller	Perform a heat treatment at 140~150°C for 1hr and let sit for 48±4hrs at room temperature. Perform the initial measurement.

15. Damp heat with load

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change $\leq \pm 7.5\%$ or ± 0.75 pF, whichever is larger. Cp < 30 pF, Q $\geq 100 + 10/3$ *Cp Cp ≥ 30 pF, Q ≥ 200 R*C ≥ 500 M Ω or 25 Ω •F, whichever is smaller	Test temperature: $40\pm2^{\circ}C$ Humidity: $90\sim95\%$ RH Voltage: 100% of the rated voltage Testing time: 500 ±12hrs
X7R/X5R	No remarkable visual damage Cp change $\leq \pm 12.5\%$ DF: Not more than 2 times of initial value R*C $\geq 500M\Omega$ or 25Ω •F, whichever is smaller	Measurement to be made after being kept at room temperature for $24\pm 2hrs$ (COG) or $48\pm 4hrs$ (X7R, X5R, Y5V)
Y5V	No remarkable visual damage Cp change≤ \pm 30% DF: Not more than 1.5 times of initial value R*C≥500MΩ or 25Ω•F, whichever is smaller	*Apply the rated DC voltage for 1 hour at $40\pm2^{\circ}C$. Remove and let sit for 48 ± 4 hrs at room temperature. Perform the initial measurement.

16. Life Test

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change $\leq \pm 3\%$ or ± 0.3 pF, whichever is larger. Q ≥ 350 (Cp ≥ 30 PF) Q $\geq 275+(2.5*$ Cp) (10 pF \leq Cp < 30 PF) Q $\geq 200+10*$ Cp (Cp < 10 PF) R*C ≥ 1000 M Ω or 50 Ω •F, whichever is smaller	Test temperature: Max. Operating Temp. ±3℃ Voltage: 200% of the rated voltage Testing time: 1000 hrs
X7R/X5R	No remarkable visual damage Cp change≤±12.5% DF:Not more than 2 times of initial value R*C≥1000MΩ or 50Ω•F, whichever is smaller	Measurement to be made after being kept at room temperature for 24±2hrs (COG) or 48±4hrs (X7R, X5R,Y5V) *Initial measurement for high dielectric constant type
Y5V	No remarkable visual damage Cp change≤±30% DF:Not more than 1.5 times of initial value R*C≥1000MΩ or 50Ω•F, whichever is smaller	Apply 200% of the rated DC voltage for one hour at the maximum operating temperature $\pm 3^{\circ}$ C. Remove and let sit for 48 ± 4 hrs at room temperature. Perform the initial measurement



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Packing Details

1. Tape Packing:

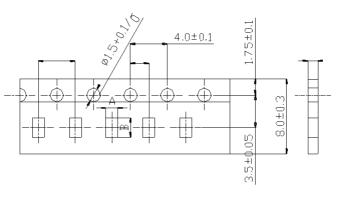
Paper Tape: Standard taping (8mm paper width) suitable to 0603,0805,4Kpcs/reel To 0402,

10Kpcs/reel.

Plastic Tape: Suitable 0805, 1206 sizes, for chip thickness over 0.95 mm, 4Kpcs/reel or

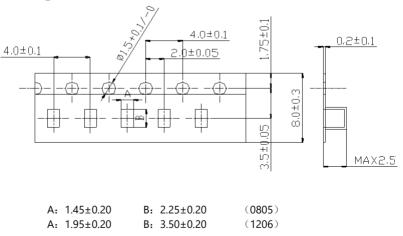
3Kpcs/reel are available.

2. Dimensions of Packing Paper:



Туре	А	В	С	D	т
0402	0.65±0.10	1.15±0.10	2.0±0.05	2.0±0.05	0.8max
0603	1.05±0.10	1.85±0.10	4.0±0.10	2.0±0.10	1.1max
0805	1.55±0.15	2.3±0.15	4.0±0.10	2.0±0.10	1.1max
1206	1.95±0.15	3.5±0.15	4.0±0.10	2.0±0.10	1.1max

3. Dimensions of Embossed Packing:





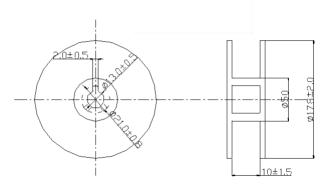
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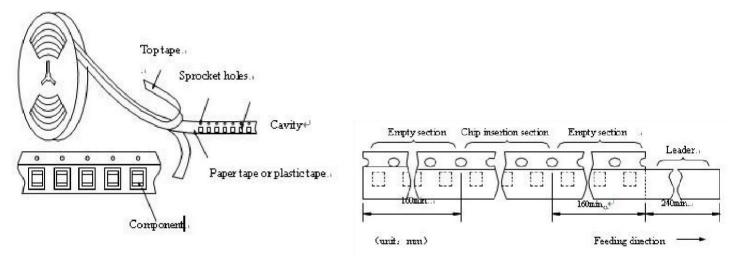




4. Dimensions of Reel:



5. Taping Figure:



6. Taping Method

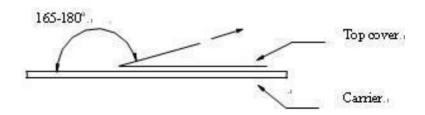
①Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.

2 The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.

3 Part of the leader and part of the empty tape shall be attached to the end of the tape as follows.

(4) Missing capacitors number within 0.1% of the number per reel or 1pc, whichever is greater, and are not continuous. (5) The top tape and bottom tape shall not protrude beyond the edges of the tape and shall not cover sprocket holes. (6) Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm.

 $\ensuremath{\overline{\mathcal{O}}}$ Peeling off force: 0.1 to 0.6N in the direction shown down. Note: Specifications are subject to change without notice.





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